Dr. V. Sumalatha
M.Tech., Ph. D.

Professor of ECE & Director, Industrial Relations & Placements
Jawaharlal Nehru Technological University Anantapur
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Bio-data

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		dirap@jntua.ac.in
Nationality	:	Indian
Religion	:	Hindu

Educational Qualifications:

S. No.	Qualification	University/Institution	Specialization	Year of award
1.	Ph. D	JNTU Anantapur	Wireless Networks	2010
2.	M.Tech	JNTU College of Engineerig. Anantapur	Digital Systems & Computer Electronics	2001
3.	B.Tech	JNTU College of Engineerig. Anantapur	Electronics & Communication Engineering	1993

Professional Experience   :

S.No.	Institution/Organization	Position held	Duration
1	JNTUA College of Engineering, Anantapur	Professor Department of ECE	2014 - till date
2	JNTUA College of Engineering, Anantapur	Associate Professor Department of ECE	2011 - 2014
3.	JNTU College of Engineering, Anantapur	Assistant Professor (Senior Scale) Department of ECE	2005 - 2011
4	JNTU College of Engineering, Anantapur	Assistant Professor Department of ECE	2000 - 05

Administrative Experience   :

S.No.	Administrative Post held	Duration
1.	Director, Industrial Relations & Placements JNTU Anantapur	2018 – till date
2.	Head, Department of ECE JNTUA College of Engineering, Anantapur	2015 – 2018
3.	Coordinator, Academic & Planning JNTU Anantapur	2012 - 2015
4.	Training and Placement Officer JNTUA College of Engineering, Anantapur	2008 – 2012

Other Responsibilities:

- ❖ University Nodal Officer for MHRD – All India Survey of Higher Education (AISHE) from 2011 to till date.
- ❖ University Program Coordinator for JNTUA – Texas Instruments University Program from 2015 – 2018.
- ❖ Chairman (UG & PG), Board of Studies, Electronic & Communcation Engineering, JNTUA College of Engineering, Anantapur from 2015 – 2018.
- ❖ Chairman (UG), Board of Studies, Electronic & Communcation Engineering, JNTU Anantapur from 2016 – 2019.

Activities as the Training and Placement Officer   :

Served as Training & Placement Officer of JNTUA CEA during 2008 – 2012:

- ❖ During this period, more than 800 students are successfully placed in various companies. Recoded highest placements in terms of number of offers made (410) in an academic year & also in terms of number of companies(20) visited.
- ❖ During this period, college is accredited as trusted academic partner by WIPRO Technologies & also by Cognizant Technology Solutions. Further, Cognizant has also provided scholarships to meritorious & economically backward students..
- ❖ Organized several programmes to bridge the gap between the institution & industry.
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Coordinator, Academic & Planning:

Served as Coordinator, Academic & Palnning, JNTUA during 2012 - 15:.

- ❖ Convener & Member of University Purchase Committee, established & streamlined purchase procedures for the University under the guidance of the Director, Academic & Planning.
- ❖ Maintaining & furnishing all statistical data for submission to the Central and State Government.
- ❖ Assisting the Director of Academic & Planning in constituting committees for FFC visits & generating deficiency reports of the colleges.
- ❖ Preparing Convocation report of the University & printing of diary.
- ❖ Issues related to Student transfers & equivalent subjects for readmitted students.

- ❖ Assisting the Director of Academic & Planning in curriculum revision, convening BoS meetings.
- ❖ Assisting the Director of Academic & Planning in conducting ratification process pertaining to affiliated colleges.
- ❖ Maintaining the University Website & DAP Portal.

Head of the ECE Department:
Served as Head of ECE department, JNTUACEA during 2015 - 18:.

- ❖ Deaprtment is accredited by NBA for three years from 2016-19.
- ❖ Established VLSI laboratory with high end computers, Cadence & Mentor Grahics tools.
- ❖ Improvement in the ambience of the department
- ❖ Organized several programs for the students & faculty.
- ❖ Revision of UG & PG syllabi as Chairman – BoS for the autonomous institute

Director, Industrial Realtions & Placements: (2018 – till date)
- ❖ Collaborations:
  - ● Academic Membership with Open Power Foundation - collaboration around Power ISA-based products initiated by IBM and opening the technology surrounding their Power Architecture offerings, such as processor specifications, firmware and software with a liberal license, and will be using a collaborative development model with their partners.
  - ● Partnership between IBM SkillsBuild and CSRBOX to enhance employment opportunities for students by building soft skills and earning industry-relevant training.
  - ● Collaboration with Efftronics Systems Pvt. Ltd., Vijayawada in starting new PG program, M.Tech.- Internet of Things at the JNTUA College of Engineering, Anantapur
  - ● Collaboration with Tata Consultancy Services to conduct workshops and training programs for faculty and students in the emerging technologies
  - ● Collaboration with TOOOPLE Pte Ltd, Singapore to provide Experiential Learning on emerging technologies to students through mentored projects virtually, using technology on TOOOPLE Platform.
- ❖ Provided internship & placement opportunities to the students of the university in various reputed industries.

- ❖ Organized several job fairs & training programs in collaboration with Andhra Pradesh Information Technology Academy (APITA) and Confederation of Indian Industry (CII).
- ❖ With the National concepet of one nation one subscription Consortium of e-Resources is started. Coordinating JNTUA Consortium of e-Resources with the Constituent & Affiliated Engineering and Pharmacy institutions for subscribing e-Resources with an objective to provide rapid and efficient access to scholarly content and other electronic teaching and learning resources with competitive pricing models and to promote inter institution interaction and co-operation.

Other Activities:

Texas Instruments University Program Coordinator of JNTUA (2015-2018):

Texas Instruments Centre of Excellence (CoE) is established in three constituent Engineering colleges of the Univeristy.

The following progams are organized for faculty & students during 2015 – 2018 through CoEs:

- ❖ Three day Faculty development program on "Linear Integrated Circuits – A Systems Approach" in collaboration with Sapience Consulting – University Program Partners of Texas Instruments during 2-4, Mar 2015.
- ❖ One day Workshop on "Analog System Design using ASLK" by the faculty mentors trained earlier by Texas Instruments on 15th July 2015.
- ❖ Three day Workshop on "Embedded System Design using MSP430" in collaboration with Sapience Consulting – University Program Partners of Texas Instruments during 10-12, September 2015.
- ❖ Online Contest in association with Texas Instruments India on 17th March, 2016. Around 500 students of various colleges participated in the contest. TI Chronos watches, Launch pads and merit certificates were given to the top 10 winners of the online contest.
- ❖ One day training for the students on WEBENCH & MSP430. Around 100 students of various colleges participated in the program.
- ❖ Two day hands on workshop on "Embedded System Design using MSP430" by the mentors trained earlier by Texas Instruments during 24-25 June 2016. Around 55 faculty members were trained.
- ❖ Organized weekly online contests, DrishTI in collaboration with Texas Instruments & Sapience Consulting in the year 2017. Several students have won cash prizes and awarded merit certificates in DrishTI online contests.
- ❖ Initiated entrepreneurship activity, ShrisTI in collaboration with Texas Instruments & Sapience Consulting in the year 2017.

- ❖ Three day Workshop on "Embedded System Design using TIVA" in collaboration with Sapience Consulting – University Program Partners of Texas Instruments during 19-21, April and 31 May – 2 June, 2018.

Teaching Experience: **18**+ years of teaching UG & PG Courses

Subjects Taught:

Undergraduate courses	Post graduate courses
❖ Electronic Devices and Circuits ❖ Electronic Circuit Analysis ❖ Network Analysis ❖ Switching Theory & Logic Design ❖ Linear IC Applications ❖ Data Structures ❖ Pulse & Digital Circuits ❖ Telecommunication Switching Systems & Networks ❖ Computer Networks ❖ Neural Networks & Applications	❖ Digital Data Communications ❖ Digital System Design ❖ Artificial Neural Networks ❖ OOPs through JAVA ❖ High Speed Networks ❖ Advanced Computer Networks ❖ Fuzzy Systems & Neural Networks

- ❖ In addition, the laboratories of UG and PG programmes in the areas of Electronics and Computers have been dealt with.

Books Authored:
- ❖ Chennakesavulu M, T. Jayachandra Prasad, V, Sumalatha, " Improved Performance of On-chip data interconnects", by LAMBERT Academic Publishing, ISBN 978-620-2-67248-1, 2020.
- ❖ Rajasekhar Butta, M. Kamaraju, Sumalatha V, authored a book chapter "Heuristic methods for data clustering" published in the Book entitled Artificial Intelligence in Data Mining Theories and Applications, Academic Press, an imprint of Elsevier, pp 65-86, 2021.
- ❖ Rajasekhar Butta, M. Kamaraju, Sumalatha V, authored a book chapter "Speaker Independent Emotion Recognition from Speech Signals" accepted for publication in the book entitled Advanced Aspects of Engineering Research, B P International, 2021.

Patents Published:
- ❖ B. Rajasekhar, M. Kamaraju, V. Sumalatha published a Patent on title "Intelligent Customer Care Centre", Appplication Number: 201941054213

❖ B. Rajasekhar, M. Kamaraju, V. Sumalatha published a Patent on title "Multimodal Voice based Authentication Framework", Application No.: 202141007889 A

Sponsored Research Projects completed as Co-ordinator:

❖ Completed AICTE project titled " Performance Analysis of Algorithms for Multi Sensor Image Fusion & Optimization of Architecture for VLSI Implementation" under Research Promotion Scheme (RPS) for sanctioned amount of Rs.14 Lakhs in the year 2015.

Number of Research publications:

International Journals	:	43
International Conferences	:	26
National Conferences	:	08

Performance Indices of Publications :

Total Citations	:	137
h - Index	:	7
i10 - Index	:	4

List of publications:

International Journals:

1. Sharath Babu Rao, D., Sumalatha, V "A 600 mV + 12 dBm IIP3 CMOS LNA with Gm Smoothening Auxiliary Path for 2.4 GHz Wireless Applications", 2021, EAI/Springer Innovations in Communication and Computing, pp. 241-254
2. Kumar, N.P., Charles, B.S., Sumalatha, V., "Implementation of cache memory and fir filter using FINFETs at 22nm Technology for SOC designs", 2020, Microprocessors and Microsystems
3. Harikishore, S., Sumalatha, V "A reliable multi-hop opportunistic routing scheme with bandwidth guarantee for multimedia wireless mesh networks", 2020, Journal of Ambient Intelligence and Humanized Computing
4. Praveen Kumar, N., Stephen Charles, B., Sumalatha, V. "Leakage current analysis of 6t & 7T-SRAM using finfets at 22nm technology", 2019, International Journal of Innovative Technology and Exploring Engineering, 8(12), pp. 2983-2986
5. Harikishore, S., Sumalatha, V "Adaptive load balance metric for opportunistic multimedia wireless mesh networks", 2019, Journal of Theoretical and Applied Information Technology 97(13), pp. 3650-3660

6. Neeraja, Y., Sumalatha, V "Collision-aware cooperative MAC protocol design for mobile ad-hoc networks", 2019, Indonesian Journal of Electrical Engineering and Computer Science, 14(1), pp. 455-461

7. Chennakesavulu, M., Jayachandra Prasad, T., Sumalatha, V "Data encoding techniques to reduce the switching activity and crosstalk delay of on-chip data interconnects", 2019, International Journal of Recent Technology and Engineering, 7(ICETESM18), pp. 59-64

8. Kumar, P.S., Jaya Sankar, K., Sumalatha, V. "Design and implementation of announcement procedure for wireless sensor network", 2019 Journal of Advanced Research in Dynamical and Control Systems, 11(11), pp. 769-779

9. Neeraja, Y., Sumalatha, V "Priority based QoS-aware medium access control protocol for mobile Ad-Hoc networks", 2019 Intelligent Systems Reference Library 172, pp. 145-153

10. Harikishore, S., Sumalatha, V "Ant colony optimization based energy efficiency for improving opportunistic routing in multimedia wireless mesh network", 2019 Indonesian Journal of Electrical Engineering and Computer Science 16(3), pp. 1371-1378

11. Prathiba, B., Jaya Sankar, K., Sumalatha, V "Novel framework of retaining maximum data quality and energy efficiency in reconfigurable wireless sensor network", 2019, International Journal of Electrical and Computer Engineering, 9(4), pp. 2893-2901

12. Rajasekhar, B., Kamaraju, M., Sumalatha, V. "Glowworm swarm based fuzzy classifier with dual features for speech emotion recognition", 2019 *Evolutionary Intelligence*

13. Prathiba, B., Jaya Sankar, K., Sumalatha, V "SDQE: Sensor data quality enhancement in reconfigurable network for optimal reliability", 2019 Advances in Intelligent Systems and Computing 765, pp. 356-363

14. Prathiba, B., Sankar, K.J., Sumalatha, V "A novel clustering algorithm for leveraging data quality in wireless sensor network", 2018 Communications in Computer and Information Science 827, pp. 687-694

15. Pattem, S.K., Jayasankar, K., Sumalatha, V "Probabilistic security analysis through path-based metric evaluation in wireless sensor networks", 2018, International Journal of Engineering and Technology(UAE) 7(2), pp. 149-155

16. M.Chennakesavulu, T.Jayachandra Prasad, V.Sumalatha, "Data encoding techniques to improve the performance of system on chip" *Journal of King Saud University, Compute and Information Science*s, DOI: 10.1016/j.iksuci.2018.12.003, 1319-1578, 2018.

17. Prathiba. B, Jayasankar.K, Sumalatha.V, "Performance Enhancement of Wireless Sensor Networks based on a Novel Data Quality Framework-An Approach", *International Journal of Research in Electronics and Computer Engineering*, vol. 6, issue 4, ISSN: 2348-2281, 2018.

18. Nemani, Subash, Jayachandra Prasad Talari, Sumalatha V, "Reversible Data Hiding Using Secure Force Algorithm" *Journal of Advanced Research in Dynamical and Control System,* vol,10, special issue, pp 238-244, ISSN 1943-023X, 2018.

19. Prathiba. B, Jayasankar.K, Sumalatha.V "Framework For Assessing Data Quality Resembles Factor In Large Scale Wireless Sensor Network", *International Journal of Engineering & Technology*, DOI:10.14419/ijet.v7i4.19280, 2018.

20. P. Sampath Kumar, K. Jaya Sankar, V. Sumalatha "Propagation Model for Synchronous High Throughput Communication in Wireless Sensor Network", *Journal of advanced Research in Dynamical and Control Systems*, vol.10, issue.4, pp :263- 277, ISSN:1943-023X. Mar 2018.

21. Nemani, Subash, Jayachandra Prasad Talari, Sumalatha V,"13/9 Lifting Wavelet Transform based Reversible Data Hiding", *International Journal of Computer Applications.,* vol. 53, pp. 20-26, 0975 – 8887, 2018.

22. B. Rajasekhar, M. Kamaraju, V. Sumalatha, "Speaker Independent Emotion Recognition from Speech Signals", *ARPN Journal of Engineering and Applied Sciences* ,vol. 13, no. 9, pp: 3188-3191, ISSN: 1819-6608, May 2018.

23. M.Chennakesavulu, T.Jayachandra Prasad, V.Sumalatha, "Improved performance of error controlling codes using pass transistor logic", *Circuits Systems and Signal Processing (SCIE)*, vol. 37,issue 3, Pp: 1145-1161, 0278-081X, Mar.2018.

24. Ajitha,D, K.V.Ramanaiah,V. Sumalatha "An Enhanced High Speed Multi-Digit BCD Adder using Quantum-dot Cellular Automata », *Journal of Semiconductors*, vol. 38, no.2, pp 1-9. 1674-4926, Feb. 2017.

25. Nemani, Subash, Jayachandra Prasad Talari, Sumalatha V, " Distortionless Data Hiding for Encrypted Images: A DCT Approach", *Journal of Engineering Science and Technology Review*, vol.10, issue 5, pp. 61-67, ISSN: 1791-2377, 2017.

26. Ajitha,D, K.V.Ramanaiah, V. Sumalatha "An Optimized Hybrid Multi-Digit BCD Adder Using QCA." *International Journal of Computer Science and Information Security*, vol. 14, no. 7, pp 589-598. 1947-5500, July 2016.

27. D Sharath Babu Rao, V Sumalatha, "The Journey of Ethernet Towards 1 TBPS and Various Fields of Applications", Indian Journal of Science and Technology, vol 9, no.11, DOI: 10.17485/ijst/2016/v9i11/89293, March 2016.

28. P. Sampath Kumar, K. Jaya Sankar, V. Sumalatha " High throughput system modeling for WSN", *International Journal of Computer Networks And Security*, vol.25, issue.2, pp:1365- 1373, ISSN : 2051-6878, , July 2015.

29. P. Sampath Kumar, K.Jaya Sankar, V.Sumalatha "Network Synchronization Coding for Wireless Sensor Network Device", *International Journal of Computer Science and Network Security,* vol.14, no. 6, pp. 127-134,ISSN: 1738 -7906, June 2014.

30. G.Mamatha, M.V.Lakshmaiah, V.Sumalatha, Cyril Prasanna Raj P, "Evaluation of DWT based image Fusion with three Different Re-sampling Methods", *International Advanced Research Journal in Science, Engineering and Technology,* vol. 2, issue 2, Feb. 2015.

31. Ajitha, D, K. V. Ramanaiah, V. Sumalatha, "An Efficient Design of XOR Gate and its Applications using QCA*" i-Manager's Journal on Electronics Engineering*, 2015.

32. G.Mamatha, M.V.Lakshmaiah, V.Sumalatha, S.Varadarajan, "DWT Based Pan-Sharpening Of Low Resolution Multispectral Satellite Images", *i-Manager's Journal on Image Processing, vol 1, no.2*, June 2014.

33. P. Sampath Kumar, K.Jaya Sankar, V.Sumalatha " Controller Design For Energy Scheduling In Wireless Sensor Network Device Modeling", *International Journal of Engineering & Technology*, ISSN: 2227-524X, 2014.

34. Y. Neeraja, V. Sumalatha, "Comprehensive Survey of Medium Access Control Protocols for MANETs" *International Journal of Emerging Trends & Technology in Computer Science,* vol. 2, issue 3, pp. 211-219, ISSN 2278-6856, June 2013.

35. P. Sampath Kumar, K. Jaya Sankar, V.Sumalatha *"Design and implementation of communication protocol for WSN"*, International Journal of Technology Intelligence and Planning, ISSN :1740-2840, 2013.

36. Ajitha, D, Ramanaiah, K. V., Kumar, P. Y, &Sumalatha, V. "Design of Compact and High Speed Baugh-Wooley Multiplier by CSA using QCA", *International Journal of Engineering Research and Technology,* vol. 2, no. 11, Nov. 2013.

37. C.Ravishankar Reddy, V.Sumalatha, P.V. Gopi Kumar "Fault Detection by using Random and Deterministic test pattern techniques in a mixed mode BIST environment" *International Journal of Research and Application*, vol. 2 , issue 5 , pp. 720-726 , Oct. 2012.

38. C.Ravishankar Reddy, V.Sumalatha, Shaik zilani "Low power, Low transition random pattern generator" *International Journal of Engineering Research & Technology*, vol.1, issue 5, July 2012.

39. Dilli Ravilla, P.C.Reddy, V.Sumalatha, "An Adaptive hybrid framework for Ad- Hoc Wireless Networks" *International Journal of Recent Trends in Engineering and Technology*, vol 4 , no.3, pp. 86-91, Nov 2010.

40. G. Srinivas Rao, V.Sumalatha, "Performance Analysis of Voice quality aspects in a wireless LAN Network" *The Journal of Computing,* vol , issue 6, pp.33 – 39, Oct 2010.

41. V.Sumalatha, V.Venkateswar Reddy," Design and implementation of Self checking carry select adder" *Journal of the Technology world*, vol. 5, issue 1, pp.19-24, Mar.2010.

42. V.Sumalatha, P.C. Reddy, "A Novel Approach for mis-behaviour Detection in Adhoc Networks" *International Journal of Cryptography and Security*, vol. 2, no.1, pp.17-24, Jan. 2009.

43. V.Sumalatha, P.C.Reddy, "Integrated Energy Aware Mechanism for On-demand Routing Protocols" *International Journal of Engineering Research and Industrial Applications*, vol.1, pp. 367- 381, 2008.

International Conferences:

1. Prathiba. B, Jayasankar.K, Sumalatha.V, "A Novel Cluster Algorithm for Leveraging Data Quality in Wireless Sensor Network", *Cybernetics and Algorithms in Intelligent Systems*, Seventh Computer Science, ISSN 2194-5357 ISSN 2194-5365, 2018.

2. Nemani, Subash, Jayachandra Prasad Talari, Sumalatha , "Estimation of performance metrics for Reversible Data Hiding before encryption", *IEEE Conference on Intelligent Computing and Control Systems,* pp.1176-1181, 978-1-5386-2745-7/17, 2017.

3. B. Rajasekhar, M. Kamaraju, V. Sumalatha, "FPGA based Emotions Recognition from Speech Signals", *IEEE sponsored third International Conference on Bio-signals, Images and Instrumentation,* 16-18 Mar. 2017

4. Prathiba. B, Jayasankar.K, Sumalatha.V, "A Novel Cluster Algorithm for Leveraging Data Quality in Wireless Sensor Network", *Smart and Innovative Trends in Next Generation Computing Technologies*, Dehradun, 2017.

5. D Sharath Babu Rao, V Sumalatha, "Design of Low Noise Amplifier with Integrated Mixer for 2.4 G Hz Wireless Applications", *International Conference on Inventive Research in Emerging Technology*, 2017.

6. Prathiba. B, Jayasankar.K, Sumalatha.V, "Enhancing Data Quality in Wireless Sensor Networks-A Review", *International Conference on Automatic Control and Dynamic Optimization Techniques,* ISBN: 978-1-5090-2081-2, 2017.

7. P. Sampath Kumar, K. Jaya Sankar, V.Sumalatha, "A Power Efficient Communication Protocol for WSN ", *International Conference on Computing Methodologies & Communication,* ISBN:978-1-5090-4890-8, July 2017.

8. D Sharath Babu Rao , V Sumalatha, "The Elements of the Ethernet : Proliferation of data rates through the Bridge Layer & MAC Protocol stack" , *International Conference on Inventive Computation Technologies*, 26-27 Aug. 2016.

9. P. Sampath Kumar, K. Jaya Sankar, V.Sumalatha, "A Power Efficient Communication Protocol for WSN", *International Conference on Computing methodologies & Communication*, ISBN:978-1-5090-4890-8, July 2017.

10. P. Sampath Kumar, K. Jaya Sankar, V.Sumalatha "Optimal synchronous node design with traffic governance in WSN ", *International Conference on Communication and Signal Processing,* ISBN: 978-1-5090-0396-9, April 2016.

11. C. Ravishankar Reddy, V. Sumalatha, "Hybrid Type Test Patter Generator for Memory Testing", *International Conference on Electricals, Electronics, Signals Communication and Optimization,* DOI: 10.1109/EESCO.2015.7253652, 2015.

12. Ajitha, D., K. V. Ramanaiah, V. Sumalatha. "A Novel design of Cascading Serial bit-stream Magnitude Comparator using QCA" *International Conference on Advances in Electronics, Computers and Communications,* 2014.

13. C. Ravishankar Reddy, V. Sumalatha, "A Novel Built in Self Test Pattern Generator for Low Power Dissipation, High Fault Coverage and Reduced Area Overhead", *International Conference on Recent Advances published in the proceedings of Design, Development and Operation of Micro Air Vehicles*, Hyderabad 21 – 23 Nov. 2013.

14. G.Mamatha, M.V.Lakshmaiah, V.Sumalatha, " Field Programmable Gate Array and Design Implementation of Image Fusion using DWT", IEEE International Conference, USA, May 2013.

15. "Field Programmable Gate Array and Design Implementation of Image Fusion using DWT", IEEE International Conference USA, May 2013.

16. Ajitha, D, K. V.Ramanaiah, V. Sumalatha. "Design of High Speed, Low Area, Carry Flow BCD Adder in QCA" *IEEE International Conference on Advanced Research in Engineering and Technology*, Feb 2013.

17. C. Ravishankar Reddy, V. Sumalatha, "A New Built in Self Test Pattern Generator for Low Power Dissipation and High Fault Coverage", *IEEE International Conference on Recent Advances in Intelligent Computational Systems*, DOI: http://dx.doi.org/10.1109/RAICS.2013.6745440 , 2013.

18. V.Saritha, V.Sumalatha, "Robust *Fault Tolerance Approach for Multi-node Faults in Radial Basis Function Networks" International Conference on On-demand Computing*, Bangalore, 3-4 Nov. 2010.

19. V.Sumalatha, T. Syam Kumar, "Synchronization Enhancement to Network Processor using IEEE 1588 Protocol" *Second International Conference on RF and Signal Processing Systems*, Guntur during 7- 9 Jan. 2010.

20. V.Sumalatha, V.V. Reddy, "Self Checking Carry Adder Design" AICTE Sponsored *International Conference on Innovative Computing, Information and Communication Technology,* Chennai, 16 - 18 Dec. 2009.

21. V.Sumalatha , M.Hema, " Transmission & Enhancement of Image through OFC using DIP Techniques" International Conference on Coalescence of Computer Science & Biotechnology, Visakhapatnam, 16 - 17 Feb. 2009.

22. V.Sumalatha, Ms.Sandhya Rani, "Link Quality based Adaptive Routing for Ad-Hoc Network" *International Conference on Quantitative Methods, Operations & Information Technology*, IBS Hyderabad, 24-25 Oct. 2008.

23. G. Narendra Kumar, V.Sumalatha, " Secure Mulyipath Routing for fully distributed MANETs", *IETE Conference on RF & Wireless, ICON RFW-08*, IETE Bangalore centre, 24-26 April 2008.

24. V.Sumalatha, P.C.Reddy, "Coping with mis-behaviour of nodes in Mobile Ad-hoc Networks" *International Conference, INDICON'07*, IEEE Bangalore section, 6-8 Sept. 2007.
25. V.Sumalatha, P.C. Reddy, "Secure Routing in Mobile Adhoc Networks" *International Conference on Advances in Electronics and Communications*" 1-3, Feb. 2007.
26. V.Sumalatha, P.C.Reddy, "Mobile Ad-hoc Network Security and Authentication" *International Conference on Advanced Communication Systems*" Coimbatore, 10 – 12 Jan. 2007.

National Conferences:

1. V.Sumalatha, B.V.R.V.Prasad, Sudha Vani, "Image Segmentation by Fuzzy Means" *National Conference on Recent Trends in Electronics & Communication(NCRTEC'09)*, Bangalore, 04th April 2009.
2. V.Sumalatha, R.Sandhya, "Implementation of WISPER protocol for wireless multimedia networks" *National Conference on Computing and Communications (NCCC'08)*, Hyderabad, 28 – 29 November 2008.
3. V.Sumalatha, P.C.Reddy. K.Sudarsen Raju, "Performance Comparison of Routing Protocols for Mobile Ad-hoc Networks "*National Conference on Emerging Trends in Computer and Network Technologies (NCETCN'08),* Hospet, Karnataka,19 - 20 September 2008.
4. V.Sumalatha, P.C. Reddy, "Secure Routing for fully Distributed Mobile Ad-Hoc Networks" *4th National Conference on Signals*, Anna University, Chennai , 8 - 9 May 2008.
5. V.Sumalatha, K.Chaitanya, "A Hybrid Adaptive Routing Protocol for Ad-Hoc Wireless Networks" *National Conference on Challenges and Opportunities in Information Technology*, Mandi Gobindgarh, 29th March 2008.
6. V.Sumalatha, M.Sreenivas, "A Distributed Architecture for Mobile Ad-Hoc Networks" *National Conference on Emerging Trends in Computer Science & Engineering*, Tirupathi 12 - 13 March 2008.
7. V.Sumalatha,V.Sudheer Reddy, "Securing transmission channel between two remote computers using secure shell" *ISTE sponsored National Conference on Research opportunities and Challenges in IT* (NCRC'07), Chennai, 04th April 2007.
8. V.Sumalatha, V.Vijaya Kumar, "Convoy Monitoring System" *National Conference on Advanced Technologies in Electrical & Electronics Intelligence* (ATEEI'06), Coimbatore 6 - 7 October 2006.

Programs Organized:

Coordinated the following Seminars/FDPs/Workshops/Conferences:

- ❖ A 5 - Day Online Training Program on "ASIC Front End Design & Verification" during 21 - 25 September 2020 in collaboration with Entuple Technologies, V.Vijaya Kumar, "Convoy Monitoring System" National Conference on Advanced Technologies

- ❖ Orientation Session for students on "Facets of ASIC/SoC Design and Verification" on 29th August 2020 in collaboration with Tata Consultancy Services (TCS).

- ❖ One day webinar on "VLSI Industry Trends and Opportunities" on 25th August 2020 in association with Entuple Technologies

- ❖ Basic & Advanced level Programs on Augmented and Virtual Reality in collaboration with EduRidge India. Details are as below:
  - 3-Day Basic Level Course during 16-18 August, 2019
  - 3-Day Basic Level Course during 23-25 August, 2019
  - 5-Day Advance Level Course during 4-8 September, 2019.

- ❖ Coordinated APITA - Regional level industry, academia and training partners stake holders meet at JNTU Anantapur on 25-10-2019

- ❖ 7 - day Intensive Training program on Employability Skills for the students of constituent engineering colleges during 18th to 24th December, 2018.

- ❖ Motivational session to students by renowned speaker Sri Annadata Mani on 25th July, 2018.

- ❖ Three day workshop on "NI LabVIEW Programming" from 2nd to 4th August, 2018 in association with Andhra Pradesh Information Technology Academy (APITA) & National Instruments.

- ❖ Expert session to students on Basic Engineering & Life Skills by an eminent speaker Prof. K.V.S.G. Murali Krishna on 08th August, 2018.

- ❖ District level HACKATHONs (TEKHACK-2018) on Natural Disaster Preparedness & Relief using AI & IoT technologies in association with IBM & SmartBridge during 4th to 10th September, 2018. Top three teams of each district have participated in the final round of TEKHACK-2018 conducted by the university during 11th – 12th September, 2018

- ❖ Two Day National Seminar on "Xilinx DSP Design flow & Implementation of Signal & Image Processing Applications using FPGAs" in collaboration with Coreel Technologies Pvt Ltd., Bangalore in 2013.

- ❖ Two Day National Seminar on "Wireless Networking & Communications" with faculty from reputed universities & industry experts as resource persons in 2012.

- ❖ National Level Student Technical symposia "E-Merge" 02 times (2008 & 2009).

- ❖ Two National Level Conferences on Recent Advances in Electronics & Communication Technologies in 2007 & 2012.
- ❖ Two Day Faculty Development Programme on "Embedded Systems & Technology" under TEQIP in collaboration with Honeywell Technologies, Bangalore in 2008.
- ❖ A Lecture on "VLSI Applications" by T. Sarveswara, RTL Leader, Texas Instruments and I. Surya Prabhavathi, Design Engineer, Infenion Technologies ,Bangalore.
- ❖ A Lecture for IV B. Tech. students of all branches on "Secrets of Successful Placements" by Prof. Ch. Kanaka Rao, Principal KITS Peddapuram.
- ❖ One day training program for III and IV B. Tech. students of all branches in "Personality development and soft skills" by Mr.Nimesh Varma, Tata Consultancy Services, Hyderabad.
- ❖ Alumni Meet of 2001-05 passed out batch working in various IT and Non IT sectors to interact and share their experiences with the students.
- ❖ A Technical Workshop on "Embedded Systems and Technology" for B.Tech. students by Mr. Sreenivas Adiki, Development Manager, TCS, Hyderabad.
- ❖ A lecture on "Career opportunities in Indian Military Services" by Colonel Sanjay Singh, Director in Army Recruitment Office, Guntur.
- ❖ A Three day Training Programme on "Career Planning and Preparation" by Career Path Solutions Pvt. Limited, Hyderabad.

Membership – Professional Societies:
1. Life Member, Indian Society for Technical Education
2. Fellow, IEI
3. Member – IEEE

Foreign Visits:
- ❖ Visited Bangkok, Thailand to attend Training & Placement Officers meet "Defining Dialogues" organized by Cognizant Technology Solutions in 2010.
- ❖ Visited London, United Kingdom for presenting technical paper in the IEEE Technically Co-sponsored International Conference in 2015.

Declaration:

I hereby declare that the information furnished above is true to the best of knowledge

Dr. V. Sumalatha

Date: 23-04-2019

Anantapur