



TEQIP-3
Technical Education Quality Improvement Programme

TEQIP Sponsored
Three Day On-line
Faculty Development Programme

Intellectual Property Rights: An Overview

(9th – 11th September, 2020)



Organized by
**Electronics & Communication Engineering
Department**
JNTUA College of Engineering(Autonomous)
Ananthapuramu-515 002, (A.P)
www.jntuacea.ac.in

&

Directorate of Faculty Development & IQAC
JNTUA, Ananthapuramu
www.jntua.ac.in

Chairman
Prof. P. Ramana Reddy
Head of ECE Department,
JNTUA CEA

Convener
Prof. G. Prasanthi,
Director,
Faculty Development & IQAC, JNTUA

Coordinator
Dr. S. Aruna Mastani
Assistant Professor,
Dept. of ECE, JNTUA CEA

Registration Details

No registration fee. Participants are to
register by using the link
<https://forms.gle/CwUeYzHx2BZPqNyd9>

Last Date for Registration : **06.09.2020**

Contact Details

Dr. S. Aruna Mastani
Coordinator
Ph. No. 7382720324
e-mail Id : iprfdp.jntua@gmail.com

Resource Persons



Prof. G. Prasanthi,
Director, Faculty Development & IQAC



Sri Satish Kumar
Assistant Director,
O/o Development Commissioner (MSME),
Ministry of MSME, Govt. of India



Sri Rajneesh Kumar
Chief Operating Officer,
Pusa Krishi Incubator, IARI, New Delhi



Ms. Swarna Srivastava
Research Associate (IP),
Indian Agricultural Research Institute,
New Delhi

About College

JNTUA College of Engineering (Autonomous) Ananthapuramu, an esteemed engineering college in Andhra Pradesh was established in 1946 and has been growing continuously in size with respect to intake, faculty strength, number of academic programs offered and infrastructure. It is always committed to offer value added instruction to widen the horizon of vision for the enrichment of Technical education along with skill development.

About Department

The department of Electronics and Communication Engineering is established in the year 1974. The department has come a long way since its inception, keeping pace with the changing needs and expectations of the society. At present, the department is running a full time UG program in Electronics and Communication Engineering, three PG programs in Digital Electronics & Communication Systems (DECS), VLSI and IOT specializations besides Ph.D. degree program in emerging areas of relevant technology. The department has experienced and dedicated faculty who work with lot of zeal and enthusiasm. The department conducts workshops, symposia, student and faculty development programs regularly on latest developments in the area of electronics and communication. The department takes pride in its highly placed alumni all over the globe.

Objectives of the FDP

This Programme aims to educate faculty members about IP rights and its features. The main objectives of this FDP :

- To impart basic knowledge about IP skills and IP Rights among Academicians.
- To train in patent drafting and Prior art search
- To make aware of the government schemes and incentives to Startups.

Instructions to Participants

1. Attendance of **100%** is compulsory for certification.
2. All timely updates and information will be shared through WhatsApp group.
3. Please enter a valid email id as the certificate will be mailed it.
4. Do submit the feedback form provided at the end of each day.
5. Live sessions link will shared through mail/WhatsApp
6. Participants will be provided link through email / WhatsApp

Requirements

Smart phone/Laptop/Tablet with micro phone, headset and good internet facility.

Programme Schedule

Day – I : 09.09.2020

Session – 1 : 10.30 AM -12.00 Noon
Introduction to Intellectual Property
By *Prof. G. Prasanthi*

Session – 2 : 3.00 PM -4.30 PM
Basics of IPR & Enforcement
By *Sri Satish Kumar*

Day – II : 10.09.2020

Session – 1 : 10.30 AM -12.00 Noon
Patent Prior Art Search
By *Ms. Swarna Srivastava*

Session – 2 : 3.00 PM -4.30 PM
Technology Transfer from Academic System
By *Sri Rajneesh Kumar*

Day – III : 11.09.2020

Session – 1 : 10.30 AM -12.00 Noon
Patent Filing Strategies
By *Ms. Swarna Srivastava*

Session – 2 : 3.00 PM -4.30 PM
Incentives to Startups by Govt. of India
By *Sri Satish Kumar*